

# F1429LB

100Ω DIFF-In – 50Ω SE-Out Amplifier 1.4GHz to 3.2GHz

## Description

The F1429LB is a differential input / single-ended output 1.4GHz to 3.2GHz high-gain RF amplifier. The combination of impedance translation, high gain, high linearity, and low noise performance makes this device an ideal first-stage amplifier for a variety of transmitter applications.

The F1429LB is optimized to operate with a single 5V power supply and a nominal 64mA of  $I_{CC}$ . When operated at 2.5GHz, the device provides 21.5dB typical gain with 1.9dB noise figure, +40dBm OIP3, and +22dBm OP1dB.

The F1429LB is pin-compatible with Axiro' F1429xx family of RF amplifiers (shown below). Each F1429 variant is packaged in a 2 × 2 mm 12-DFN, with a 100Ω differential RF input and 50Ω single-ended RF output impedances for ease of integration into the signal path.

| F1429xx Base Part Number | Differential Input Impedance | DC Feed | Frequency Band | Frequency Coverage |
|--------------------------|------------------------------|---------|----------------|--------------------|
| F1429LB                  | 100Ω                         | No      | Low            | 1.4GHz to 3.2GHz   |
| F1429MB                  | 100Ω                         | No      | Mid            | 3.0GHz to 4.2GHz   |
| F1429HB                  | 100Ω                         | No      | High           | 4.0GHz to 6.0GHz   |

## Competitive Advantage

- High gain and excellent gain flatness over frequency
- Standby (STBY) feature
- Differential input to directly interface with transceiver outputs

## Typical Applications

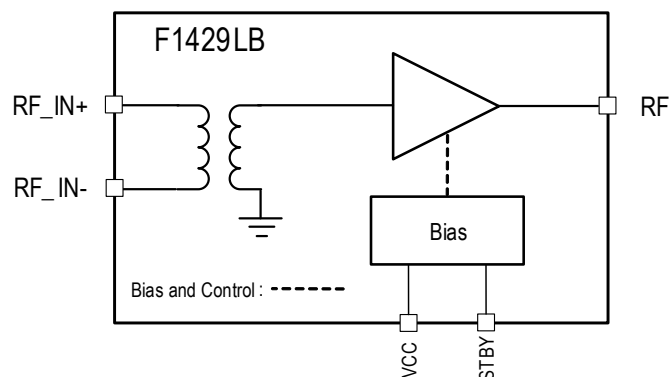
- 5G Massive MIMO base stations
- 4G TDD and FDD base stations
- Repeaters and DAS
- Point-to-point infrastructure

## Features

- RF range: 1.4GHz to 3.2GHz
- Gain = 21.5dB typical at 2.5GHz
- Noise figure = 1.9dB typical at 2.5GHz
- OIP3 = +40dBm typical at 2.5GHz
- Output P1dB = +22dBm typical at 2.5GHz
- Gain variation over temperature = ±0.2 typical
- 100Ω differential input impedance
- 50Ω single-ended output impedance
- 3.3V or 5V power supply
- $I_{CC}$  = 64mA at 5V
- 1.3mA standby current
- 1.8V and 3.3V logic support for STBY control
- Operating temperature ( $T_{EP}$ ) range: -40°C to +115°C
- 2 × 2 mm 12-DFN package

## Block Diagram

Figure 1. F1429LB Block Diagram



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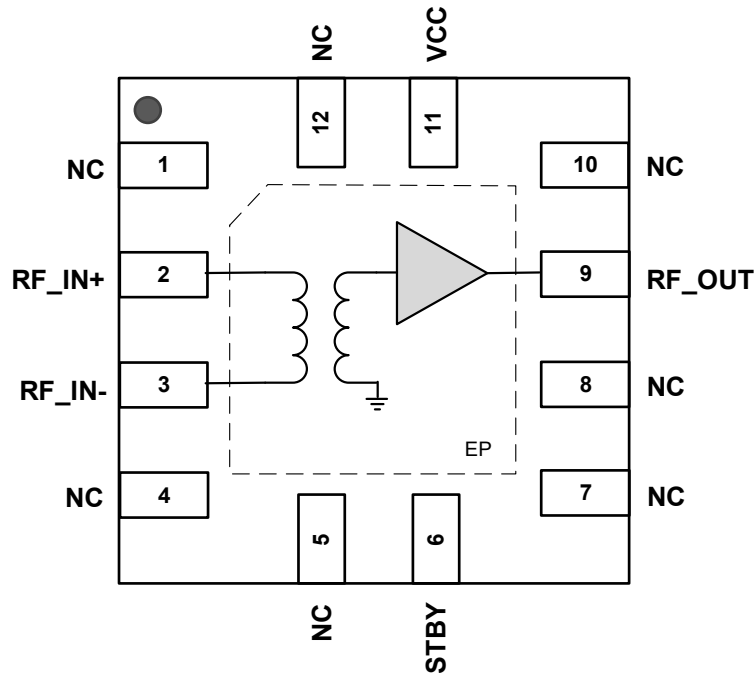
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## Pin Assignments

**Figure 2. Pin Assignments for 2 × 2 × 0.75 mm 12-DFN Package – Top View**



## Pin Descriptions

**Table 1. Pin Descriptions**

| Number                | Name   | Description                                                                                                                                                                                                                                                                                                  |
|-----------------------|--------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1, 4, 5, 7, 8, 10, 12 | NC     | No internal connection. It is highly recommended that these pins be connected to a ground via that is located as close to each pin as possible.                                                                                                                                                              |
| 2                     | RF_IN+ | Differential RF input +. Internally tied to ground. Recommendation: Use an external DC block to prevent shorting of any DC voltage that may be present on the preceding RF stage.                                                                                                                            |
| 3                     | RF_IN- | Differential RF input -. Internally tied to ground. Recommendation: Use an external DC block to prevent shorting of any DC voltage that may be present on the preceding RF stage.                                                                                                                            |
| 6                     | STBY   | Standby. If this pin is not connected or is logic LOW, the F1429LB will operate under its normal operating condition. Apply a logic HIGH for STBY.                                                                                                                                                           |
| 9                     | RF_OUT | Single-ended RF output. An external DC block is required.                                                                                                                                                                                                                                                    |
| 11                    | VCC    | Connect to the supply voltage via a choke. An external bypass capacitor must be placed as close to the pin as possible.                                                                                                                                                                                      |
|                       | – EPAD | Exposed paddle. Internally connected to ground. Solder this exposed paddle to a printed circuit board (PCB) pad that uses multiple ground vias to provide heat transfer out of the F1429LB into the PCB ground planes. These multiple ground vias are also required to achieve the specified RF performance. |

## Absolute Maximum Ratings

The absolute maximum ratings are stress ratings only. Stresses greater than those listed below can cause permanent damage to the device. Functional operation of the F1429LB at absolute maximum ratings is not implied. Exposure to absolute maximum rating conditions might affect device reliability.

**Table 2. Absolute Maximum Ratings**

| Parameter                                                                                                                                                                             | Symbol              | Minimum | Maximum                                | Units |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------|---------|----------------------------------------|-------|
| VCC to GND                                                                                                                                                                            | V <sub>CC</sub>     | -0.3    | +5.5                                   | V     |
| STBY                                                                                                                                                                                  | V <sub>STBY</sub>   | -0.3    | Lower of (5V, V <sub>CC</sub> + 0.25V) | V     |
| RF Input Power Applied for 24 Hours Maximum<br>(V <sub>CC</sub> applied, f = 2.5GHz, T <sub>EP</sub> = +115°C, and input/output VSWR = 1:1 based on a 100Ω system.) <sup>[a][b]</sup> | P <sub>MAX24</sub>  |         | +20                                    | dBm   |
| Storage Temperature Range                                                                                                                                                             | T <sub>STOR</sub>   | -65     | +150                                   | °C    |
| Lead Temperature (soldering, 10s)                                                                                                                                                     | T <sub>LEAD</sub>   |         | +260                                   | °C    |
| Electrostatic Discharge – Human Body Model (HBM)<br>(JEDEC/ESDA JS-001-2012)                                                                                                          | V <sub>ESDHBM</sub> |         | 1000                                   | V     |
| Electrostatic Discharge – Charge Device Model (CDM)<br>(JEDEC 22-C101F)                                                                                                               | V <sub>ESDCDM</sub> |         | 1500                                   | V     |

[a] Exposure to these maximum RF levels can result in significant V<sub>CC</sub> current draw due to overdriving the amplifier stages.

[b] Tested using an external 2:1 transformer at the RF input.

## Recommended Operating Conditions

**Table 3. Recommended Operating Conditions**

| Parameter                           | Symbol              | Condition                  | Minimum | Typical | Maximum | Units |
|-------------------------------------|---------------------|----------------------------|---------|---------|---------|-------|
| Power Supply Voltage <sup>[a]</sup> | V <sub>CC</sub>     | V <sub>CC</sub> pins       | 4.75    |         | 5.25    | V     |
| Operating Temperature Range         | T <sub>EP</sub>     | Exposed paddle temperature | -40     |         | +115    | °C    |
| Junction Temperature                | T <sub>JMAX</sub>   |                            |         |         | 150     | °C    |
| RF Frequency Range <sup>[b]</sup>   | f <sub>RF</sub>     |                            | 1.4     |         | 3.2     | GHz   |
| RF_IN Source Impedance              | Z <sub>RF_IN</sub>  | Differential               |         | 100     |         | Ω     |
| RF_OUT Load Impedance               | Z <sub>RF_OUT</sub> | Single-ended               |         | 50      |         | Ω     |

[a] Although the device will operate with a minimum supply voltage of 3.15V, the performance of the device has been optimized to operate within a supply voltage range of 4.75V to 5.25V.

[b] To optimize RF performance, different matching components can be used as described in the BOM.

## Electrical Characteristics – 5V Supply

See the F1429LB application circuit. Specifications apply when operated at  $V_{CC} = +5.0V$ ,  $f_{RF} = 2.5GHz$ ,  $T_{EP} = +25^{\circ}C$ , STBY = logic LOW,  $Z_S = 100\Omega$  differential,  $Z_L = 50\Omega$  single-ended, and EVKit traces and connectors are de-embedded, unless otherwise stated.

**Table 4. Electrical Characteristics with a 5V Supply**

Note: See important notes at the end of the table.

| Parameter                  | Symbol           | Condition                                                 | Minimum                   | Typical | Maximum     | Units   |
|----------------------------|------------------|-----------------------------------------------------------|---------------------------|---------|-------------|---------|
| Logic Input HIGH Threshold | $V_{IH}$         |                                                           | <b>1.17<sup>[a]</sup></b> |         |             | V       |
| Logic Input LOW Threshold  | $V_{IL}$         |                                                           |                           |         | <b>0.63</b> | V       |
| Logic Current              | $I_{IH}, I_{IL}$ | STBY pin using 1.8V logic                                 | <b>-4</b>                 |         | <b>+80</b>  | $\mu A$ |
| Supply Current             | $I_{CC}$         |                                                           |                           | 64      | <b>80</b>   | mA      |
| Standby Current            | $I_{CC\_STBY}$   |                                                           |                           | 1.3     |             | mA      |
| Settling Time              | $t_{SETTLE}$     | From 50% STBY control to within $\pm 0.5dB$ of final gain |                           | 300     |             | ns      |
| RF Input Return Loss       | $RL_{IN}$        | $f_{RF} = 1.7GHz$                                         |                           | 5       |             | dB      |
|                            |                  | $f_{RF} = 2.0GHz$                                         |                           | 11      |             |         |
|                            |                  | $f_{RF} = 2.3GHz$                                         |                           | 21      |             |         |
|                            |                  | $f_{RF} = 2.5GHz$                                         |                           | 27      |             |         |
|                            |                  | $f_{RF} = 2.7GHz$                                         |                           | 22      |             |         |
|                            |                  | $f_{RF} = 3.2GHz$                                         |                           | 15      |             |         |
| RF Output Return Loss      | $RL_{OUT}$       | $f_{RF} = 1.7GHz$                                         |                           | 7       |             | dB      |
|                            |                  | $f_{RF} = 2.0GHz$                                         |                           | 11      |             |         |
|                            |                  | $f_{RF} = 2.3GHz$                                         |                           | 13      |             |         |
|                            |                  | $f_{RF} = 2.5GHz$                                         |                           | 13      |             |         |
|                            |                  | $f_{RF} = 2.7GHz$                                         |                           | 12      |             |         |
|                            |                  | $f_{RF} = 3.2GHz$                                         |                           | 10      |             |         |
| Gain                       | $G$              | $f_{RF} = 1.7GHz$                                         |                           | 20.5    |             | dB      |
|                            |                  | $f_{RF} = 2.0GHz$                                         |                           | 22      |             |         |
|                            |                  | $f_{RF} = 2.3GHz$                                         |                           | 22      |             |         |
|                            |                  | $f_{RF} = 2.5GHz$                                         | <b>19<sup>[c]</sup></b>   | 21.5    |             |         |
|                            |                  | $f_{RF} = 2.7GHz$                                         |                           | 21.5    |             |         |
|                            |                  | $f_{RF} = 3.2GHz$                                         |                           | 20.5    |             |         |

| Parameter                                         | Symbol        | Condition                                                                                          | Minimum                                                  | Typical   | Maximum | Units |
|---------------------------------------------------|---------------|----------------------------------------------------------------------------------------------------|----------------------------------------------------------|-----------|---------|-------|
| Gain Flatness (amplitude)                         | $G_{VAR}$     | $f_{RF} = 1.7\text{GHz}, \pm 100\text{MHz}$                                                        |                                                          | 1.7       |         | dB    |
|                                                   |               | $f_{RF} = 2.0\text{GHz}, \pm 100\text{MHz}$                                                        |                                                          | 0.3       |         |       |
|                                                   |               | $f_{RF} = 2.3\text{GHz}, \pm 100\text{MHz}$                                                        |                                                          | 0.2       |         |       |
|                                                   |               | $f_{RF} = 2.5\text{GHz}, \pm 100\text{MHz}$                                                        |                                                          | 0.3       |         |       |
|                                                   |               | $f_{RF} = 2.7\text{GHz}, \pm 100\text{MHz}$                                                        |                                                          | 0.3       |         |       |
|                                                   |               | $f_{RF} = 3.1\text{GHz}, \pm 100\text{MHz}$                                                        |                                                          | 0.2       |         |       |
| Gain Variation over Temperature                   | $G_{TEMP}$    | $T_{EP} = -40^{\circ}\text{C}$ to $+115^{\circ}\text{C}$                                           |                                                          | $\pm 0.2$ |         | dB    |
| Reverse Isolation                                 | $REV_{ISO}$   |                                                                                                    |                                                          | 30        |         | dB    |
| Amplitude Imbalance                               | $IMBAL_{AMP}$ | Measures RF_IN- to RF_OUT and compares RF_IN+ to RF_OUT amplitude                                  |                                                          | 0.4       |         | dB    |
| Phase Imbalance                                   | $IMBAL_{PH}$  | Measures RF_IN- to RF_OUT and compares RF_IN+ to RF_OUT phase. Deviation is from ideal 180 degrees |                                                          | 0.8       |         | deg   |
| Common Mode Rejection                             | CMR           |                                                                                                    |                                                          | 32.5      |         | dB    |
| Noise Figure                                      | NF            | $f_{RF} = 1.7\text{GHz}$                                                                           |                                                          | 2.1       |         | dB    |
|                                                   |               | $f_{RF} = 2.0\text{GHz}$                                                                           |                                                          | 2.2       |         |       |
|                                                   |               | $f_{RF} = 2.3\text{GHz}$                                                                           |                                                          | 2.4       |         |       |
|                                                   |               | $f_{RF} = 2.5\text{GHz}$                                                                           |                                                          | 1.9       | 2       |       |
|                                                   |               | $f_{RF} = 2.7\text{GHz}$                                                                           |                                                          | 2.1       |         |       |
|                                                   |               | $f_{RF} = 3.2\text{GHz}$                                                                           |                                                          | 2.4       |         |       |
| Noise Figure Variation over Temperature           | $NF_{TEMP}$   | $T_{EP} = -40^{\circ}\text{C}$ to $+115^{\circ}\text{C}$                                           |                                                          | $\pm 0.6$ |         | dB    |
| Output Third-Order Intercept Point <sup>[b]</sup> | OIP3          | $f_{RF} = 1.7\text{GHz}$                                                                           |                                                          | 38        |         | dBm   |
|                                                   |               | $f_{RF} = 2.0\text{GHz}$                                                                           |                                                          | 37        |         |       |
|                                                   |               | $f_{RF} = 2.3\text{GHz}$                                                                           |                                                          | 37        |         |       |
|                                                   |               | $f_{RF} = 2.5\text{GHz}$                                                                           | $T_{EP} = 25^{\circ}\text{C}$                            | 40        |         |       |
|                                                   |               |                                                                                                    | $T_{EP} = -40^{\circ}\text{C}$ to $+115^{\circ}\text{C}$ | 32        |         |       |
|                                                   |               | $f_{RF} = 2.7\text{GHz}$                                                                           |                                                          | 38        |         |       |
|                                                   |               | $f_{RF} = 3.2\text{GHz}$                                                                           |                                                          | 38        |         |       |

| Parameter                                                | Symbol | Condition                | Minimum | Typical | Maximum | Units |
|----------------------------------------------------------|--------|--------------------------|---------|---------|---------|-------|
| Output P1dB                                              | OP1dB  | $f_{RF} = 1.7\text{GHz}$ |         | 21      |         | dBm   |
|                                                          |        | $f_{RF} = 2.0\text{GHz}$ |         | 21      |         |       |
|                                                          |        | $f_{RF} = 2.3\text{GHz}$ |         | 21.5    |         |       |
|                                                          |        | $f_{RF} = 2.5\text{GHz}$ | 20      | 22      |         |       |
|                                                          |        | $f_{RF} = 2.7\text{GHz}$ |         | 21      |         |       |
|                                                          |        | $f_{RF} = 3.2\text{GHz}$ |         | 21      |         |       |
| 2 <sup>nd</sup> Order Harmonic Distortion <sup>[c]</sup> | HD2    |                          |         | -45     |         | dBc   |
| 3 <sup>rd</sup> Order Harmonic Distortion <sup>[c]</sup> | HD3    |                          |         | -91     |         | dBc   |

[a] Specifications in the minimum/maximum columns that are shown in ***bold italics*** are guaranteed by test. Specifications in these columns that are not shown in bold italics are guaranteed by design characterization.

[b] OIP3 test conditions: Tone spacing = 5MHz,  $P_{OUT} = +0\text{dBm/tone}$

[c] Harmonic Distortion test conditions:  $P_{OUT} = +0\text{dBm/tone}$

## Electrical Characteristics – 3.3V Supply

See the F1429LB application circuit. Specifications apply when operated at  $V_{CC} = +3.3V$ ,  $f_{RF} = 2.5GHz$ ,  $T_{EP} = +25^{\circ}C$ , STBY = logic LOW,  $Z_S = 100\Omega$  differential,  $Z_L = 50\Omega$  single-ended, and EVKit traces and connectors are de-embedded, unless otherwise stated.

**Table 5. Electrical Characteristics with a 3.3V Supply**

Note: See important notes at the end of the table.

| Parameter                       | Symbol           | Condition                                  | Minimum             | Typical   | Maximum | Units   |
|---------------------------------|------------------|--------------------------------------------|---------------------|-----------|---------|---------|
| Logic Input High Threshold      | $V_{IH}$         |                                            | 1.17 <sup>[a]</sup> |           |         | V       |
| Logic Input Low Threshold       | $V_{IL}$         |                                            |                     |           | 0.63    | V       |
| Logic Current                   | $I_{IH}, I_{IL}$ | STBY pin using 1.8V logic                  | -4                  |           | +80     | $\mu A$ |
| Supply Current                  | $I_{CC}$         |                                            |                     | 31        |         | mA      |
| Standby Current                 | $I_{CC\_STBY}$   |                                            |                     | 1.4       |         | mA      |
| RF Input Return Loss            | $RL_{IN}$        | $f_{RF} = 1.7GHz$                          |                     | 4         |         | dB      |
|                                 |                  | $f_{RF} = 2.0GHz$                          |                     | 9         |         |         |
|                                 |                  | $f_{RF} = 2.3GHz$                          |                     | 15        |         |         |
|                                 |                  | $f_{RF} = 2.5GHz$                          |                     | 18        |         |         |
|                                 |                  | $f_{RF} = 2.7GHz$                          |                     | 17        |         |         |
|                                 |                  | $f_{RF} = 3.2GHz$                          |                     | 13        |         |         |
| RF Output Return Loss           | $RL_{OUT}$       | $f_{RF} = 1.7GHz$                          |                     | 8         |         | dB      |
|                                 |                  | $f_{RF} = 2.0GHz$                          |                     | 12        |         |         |
|                                 |                  | $f_{RF} = 2.3GHz$                          |                     | 14        |         |         |
|                                 |                  | $f_{RF} = 2.5GHz$                          |                     | 14        |         |         |
|                                 |                  | $f_{RF} = 2.7GHz$                          |                     | 14        |         |         |
|                                 |                  | $f_{RF} = 3.2GHz$                          |                     | 12        |         |         |
| Gain                            | $G$              | $f_{RF} = 1.7GHz$                          |                     | 18.5      |         | dB      |
|                                 |                  | $f_{RF} = 2.0GHz$                          |                     | 20        |         |         |
|                                 |                  | $f_{RF} = 2.3GHz$                          |                     | 20        |         |         |
|                                 |                  | $f_{RF} = 2.5GHz$                          |                     | 20        |         |         |
|                                 |                  | $f_{RF} = 2.7GHz$                          |                     | 19.5      |         |         |
|                                 |                  | $f_{RF} = 3.2GHz$                          |                     | 18.5      |         |         |
| Gain Flatness (amplitude)       | $G_{VAR}$        | $f_{RF} = 1.7GHz, \pm 100MHz$              |                     | 1.7       |         | dB      |
|                                 |                  | $f_{RF} = 2.0GHz, \pm 100MHz$              |                     | 0.4       |         |         |
|                                 |                  | $f_{RF} = 2.3GHz, \pm 100MHz$              |                     | 0.1       |         |         |
|                                 |                  | $f_{RF} = 2.5GHz, \pm 100MHz$              |                     | 0.3       |         |         |
|                                 |                  | $f_{RF} = 2.7GHz, \pm 100MHz$              |                     | 0.4       |         |         |
|                                 |                  | $f_{RF} = 3.1GHz, \pm 100MHz$              |                     | 0.5       |         |         |
| Gain Variation over Temperature | $G_{TEMP}$       | $T_{EP} = -40^{\circ}C$ to $+115^{\circ}C$ |                     | $\pm 0.2$ |         | dB      |

| Parameter                                         | Symbol               | Condition                                                                                          | Minimum | Typical | Maximum | Units |
|---------------------------------------------------|----------------------|----------------------------------------------------------------------------------------------------|---------|---------|---------|-------|
| Reverse Isolation                                 | REV <sub>ISO</sub>   |                                                                                                    |         | 29      |         | dB    |
| Amplitude Imbalance                               | IMBAL <sub>AMP</sub> | Measures RF_IN- to RF_OUT and compares RF_IN+ to RF_OUT amplitude                                  |         | 0.4     |         | dB    |
| Phase Imbalance                                   | IMBAL <sub>PH</sub>  | Measures RF_IN- to RF_OUT and compares RF_IN+ to RF_OUT phase. Deviation is from ideal 180 degrees |         | 1.2     |         | deg   |
| Common Mode Rejection                             | CMR                  |                                                                                                    |         | 32.5    |         | dB    |
| Noise Figure                                      | NF                   | f <sub>RF</sub> = 1.4GHz                                                                           |         | 4       |         | dB    |
|                                                   |                      | f <sub>RF</sub> = 1.7GHz                                                                           |         | 2.1     |         |       |
|                                                   |                      | f <sub>RF</sub> = 2.0GHz                                                                           |         | 2.2     |         |       |
|                                                   |                      | f <sub>RF</sub> = 2.3GHz                                                                           |         | 2.3     |         |       |
|                                                   |                      | f <sub>RF</sub> = 2.5GHz                                                                           |         | 1.9     |         |       |
|                                                   |                      | f <sub>RF</sub> = 2.7GHz                                                                           |         | 2.1     |         |       |
|                                                   |                      | f <sub>RF</sub> = 3.2GHz                                                                           |         | 2.4     |         |       |
| Noise Figure Variation over Temperature           | NF <sub>TEMP</sub>   | T <sub>EP</sub> = -40°C to +115°C                                                                  |         | ±0.6    |         | dB    |
| Output Third-Order Intercept Point <sup>[b]</sup> | OIP3                 | f <sub>RF</sub> = 1.7GHz                                                                           |         | 28      |         | dBm   |
|                                                   |                      | f <sub>RF</sub> = 2.0GHz                                                                           |         | 28      |         |       |
|                                                   |                      | f <sub>RF</sub> = 2.3GHz                                                                           |         | 28      |         |       |
|                                                   |                      | f <sub>RF</sub> = 2.5GHz                                                                           |         | 28      |         |       |
|                                                   |                      | f <sub>RF</sub> = 2.7GHz                                                                           |         | 27      |         |       |
|                                                   |                      | f <sub>RF</sub> = 3.2GHz                                                                           |         | 27      |         |       |
| Output P1dB                                       | OP1dB                | f <sub>RF</sub> = 1.7GHz                                                                           |         | 17      |         | dBm   |
|                                                   |                      | f <sub>RF</sub> = 2.0GHz                                                                           |         | 17.5    |         |       |
|                                                   |                      | f <sub>RF</sub> = 2.3GHz                                                                           |         | 17.5    |         |       |
|                                                   |                      | f <sub>RF</sub> = 2.5GHz                                                                           |         | 17.5    |         |       |
|                                                   |                      | f <sub>RF</sub> = 2.7GHz                                                                           |         | 17.5    |         |       |
|                                                   |                      | f <sub>RF</sub> = 3.2GHz                                                                           |         | 17.5    |         |       |

[a] Specifications in the minimum/maximum columns that are shown in **bold italics** are guaranteed by test. Specifications in these columns that are not shown in bold italics are guaranteed by design characterization.

[b] OIP3 test conditions: Tone spacing = 5MHz, P<sub>OUT</sub> = +0dBm/tone

## Thermal Characteristics

**Table 6. Thermal Characteristics**

| Parameter                                                                     | Symbol            | Value  | Units |
|-------------------------------------------------------------------------------|-------------------|--------|-------|
| Junction-to-Ambient Thermal Resistance.                                       | $\theta_{JA}$     | 112.57 | °C/W  |
| Junction-to-Case Thermal Resistance. (Case is defined as the exposed paddle.) | $\theta_{JC-BOT}$ | 27.6   | °C/W  |
| Moisture Sensitivity Rating (Per J-STD-020)                                   |                   | MSL 1  |       |

## Typical Operating Conditions (TOC)

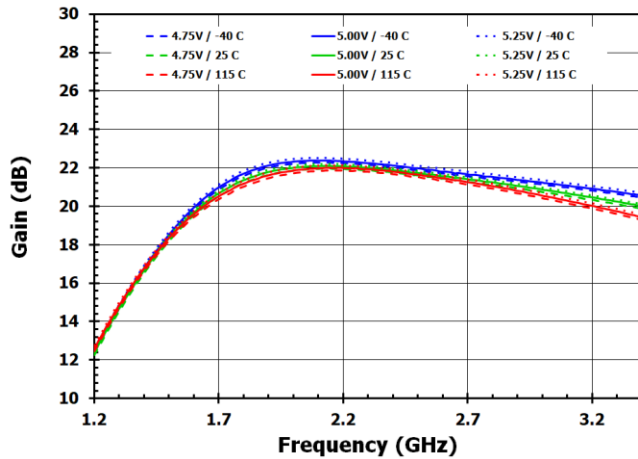
Unless otherwise noted, for the TOC graphs on the following pages, the following conditions apply:

- Evaluation kit connector and trace losses are de-embedded
- $V_{CC} = 3.3V, 5.0V$
- STBY = not connected (internally pulled to logic LOW)
- $T_{EP} = 25^{\circ}C$
- Small signal parameters measured with  $P_{OUT} = 0dBm$
- Two tone tests  $P_{OUT} = 0dBm/tone$  with 5MHz tone spacing
- All unused ports properly terminated.
- S-parameters (S11, S21, S12, and S22) measured using a de-embedded Differential Board (EVKit) with  $Z_S =$  differential 100 $\Omega$  and  $Z_L =$  single-ended 50 $\Omega$ . The inputs are mathematically combined using an ideal 1:2 (50 $\Omega$ :100 $\Omega$ ) transformer to produce the two-port S-parameters.
- OIP3, Output P1dB, and Noise Figure measured using a Transformer Board EVKit with  $Z_S = Z_L =$  single-ended 50 $\Omega$ .
- Amplitude and phase imbalances measures RF\_IN+ to RF\_OUT and compares to RF\_IN- to RF\_OUT.
- The phase imbalance is the deviation from an ideal 180 degrees.

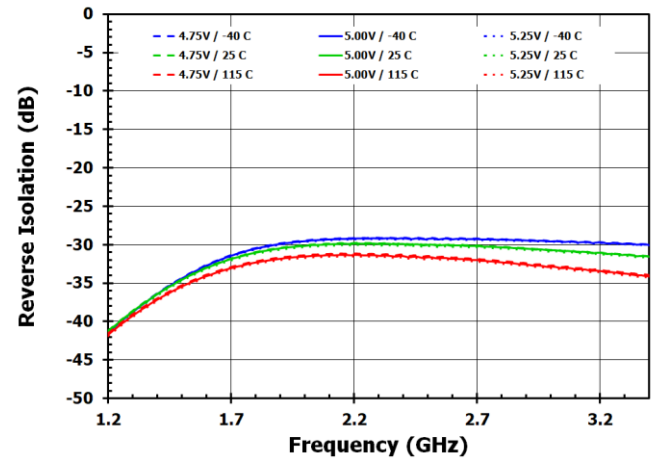
Note: The use of the external transformer T1 is included for simple two-port evaluation purposes. At some frequencies, the external transformer interacts with the on-chip balun affecting the gain and noise figure flatness responses. These interactions have been removed from the noise figure TOCs.

## Typical 5V Performance Characteristics

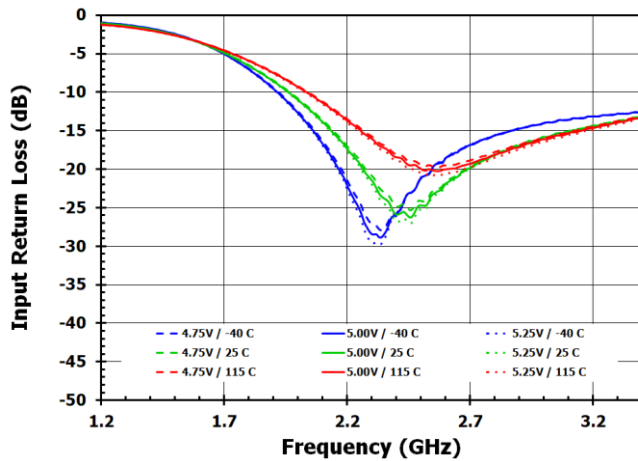
**Figure 3. Gain**



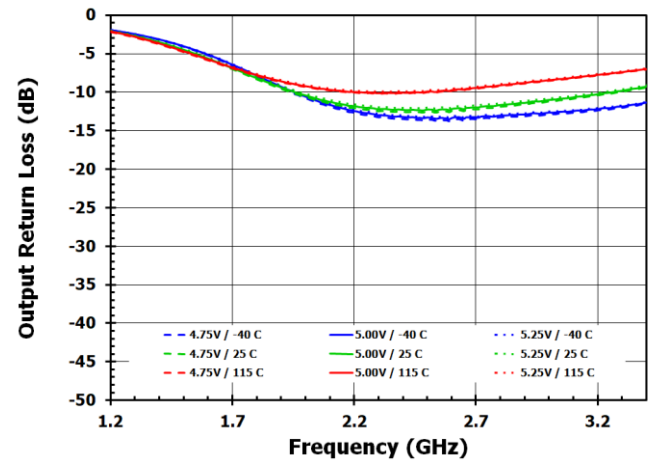
**Figure 4. Reverse Isolation**



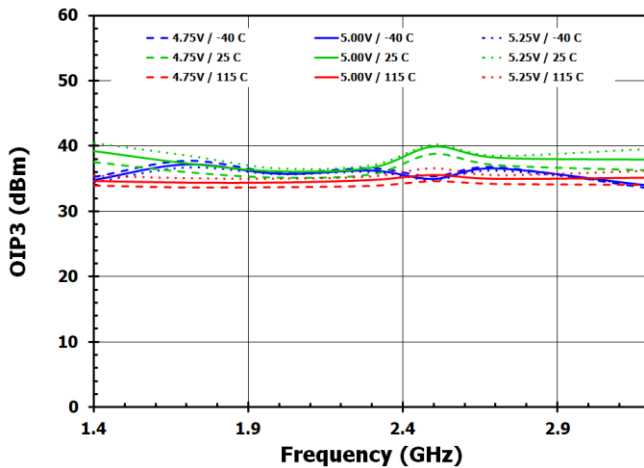
**Figure 5. Input Return Loss**



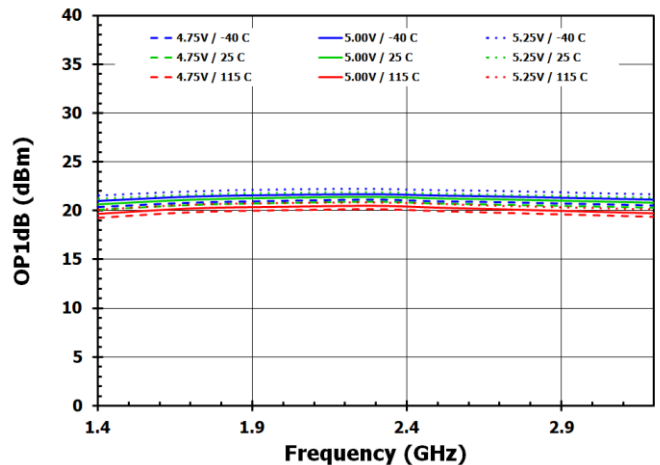
**Figure 6. Output Return Loss**



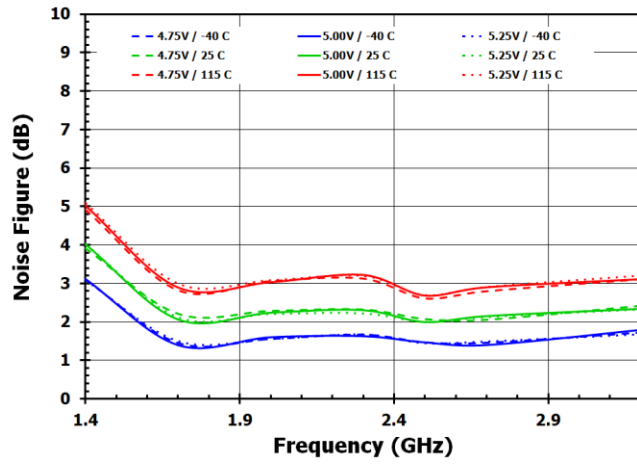
**Figure 7. Output IP3**



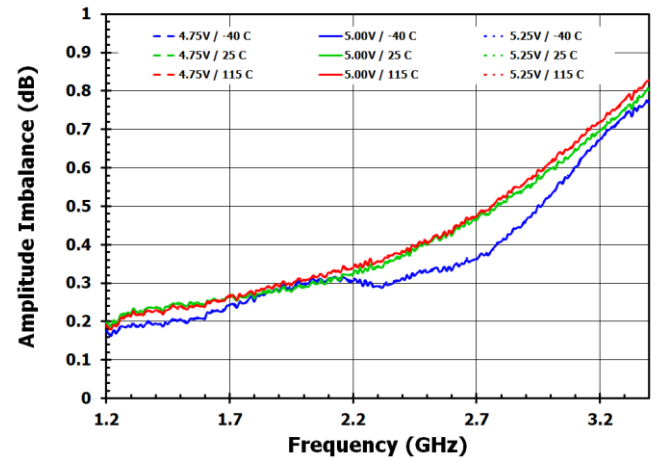
**Figure 8. Output P1dB**



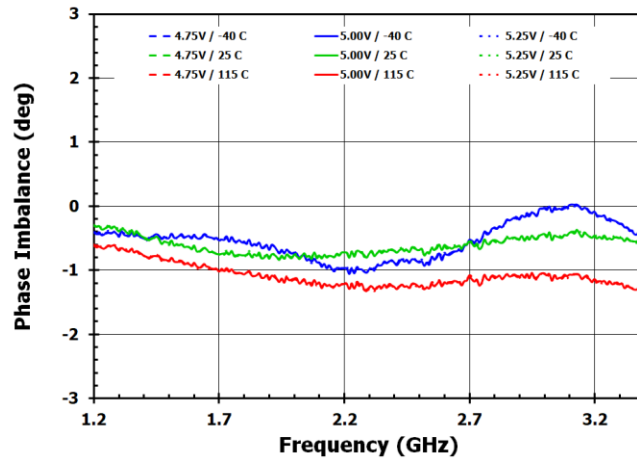
**Figure 9. Noise Figure**



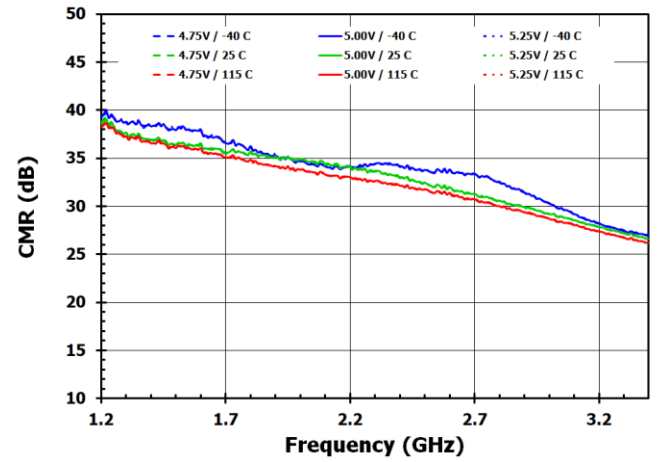
**Figure 10. Amplitude Imbalance**



**Figure 11. Phase Imbalance**



**Figure 12. CMR**



## Typical 3.3V Performance Characteristics

Figure 13. Gain

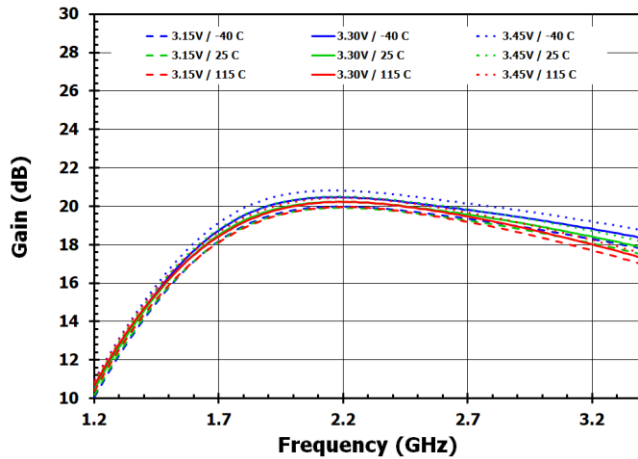


Figure 14. Reverse Isolation

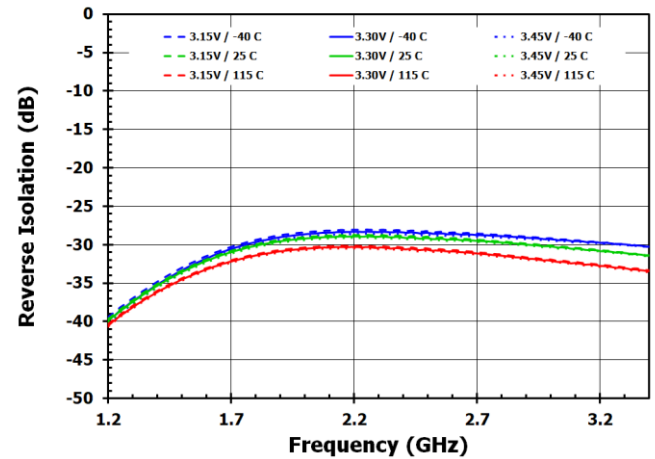


Figure 15. Input Return Loss

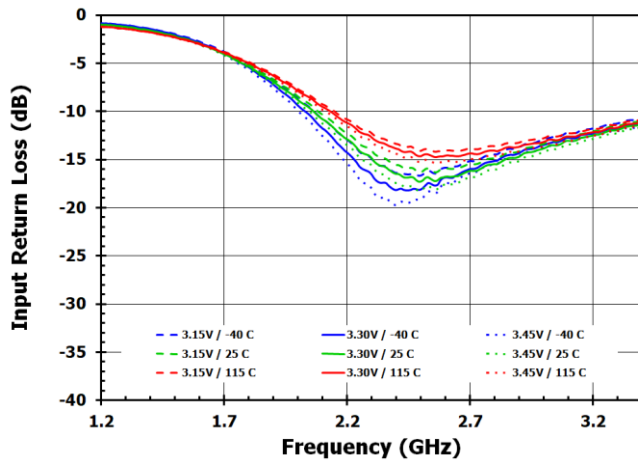


Figure 16. Output Return Loss

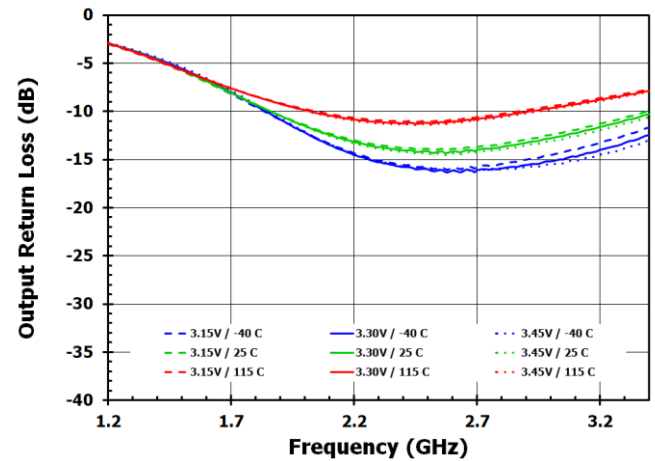


Figure 17. Output IP3

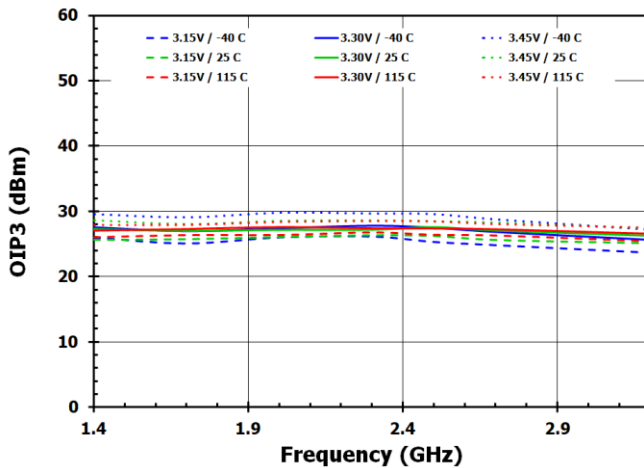
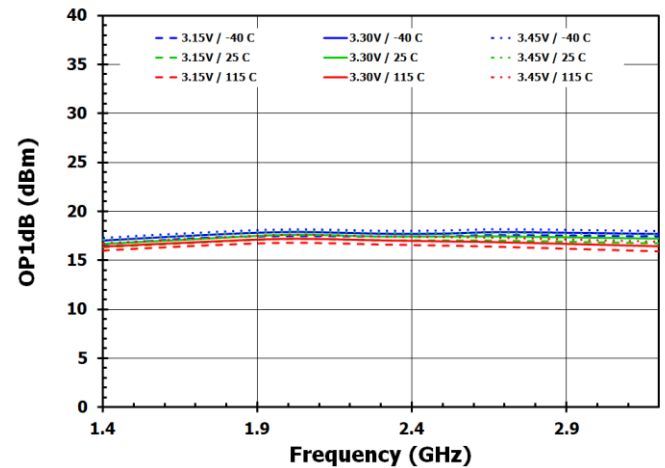
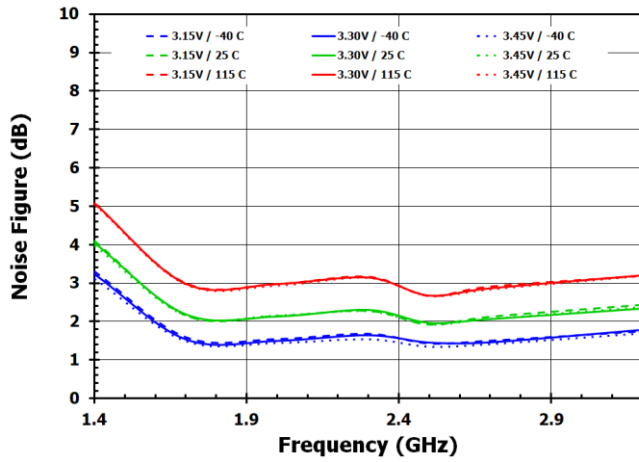


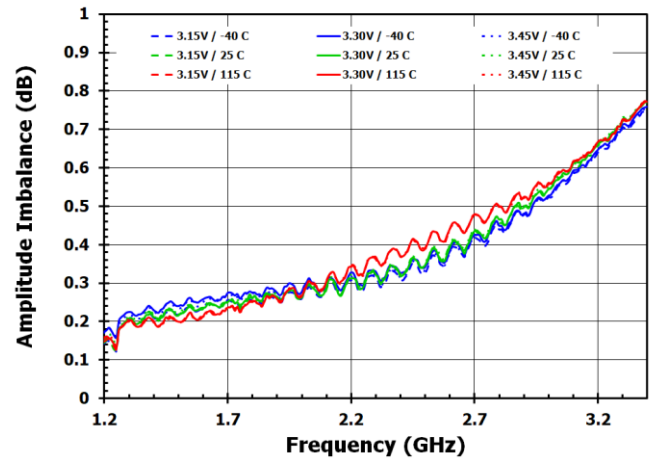
Figure 18. Output P1dB



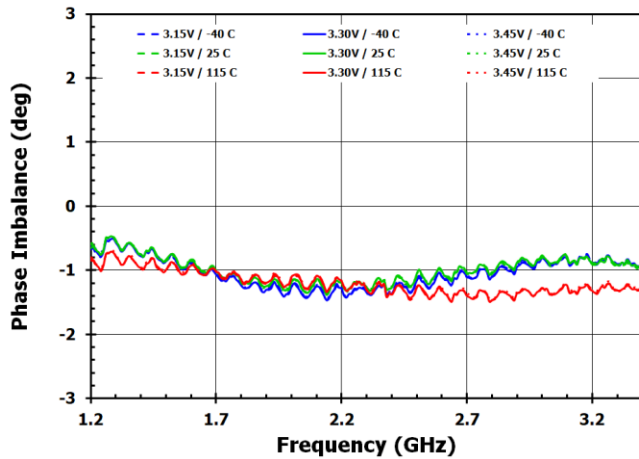
**Figure 19. Noise Figure**



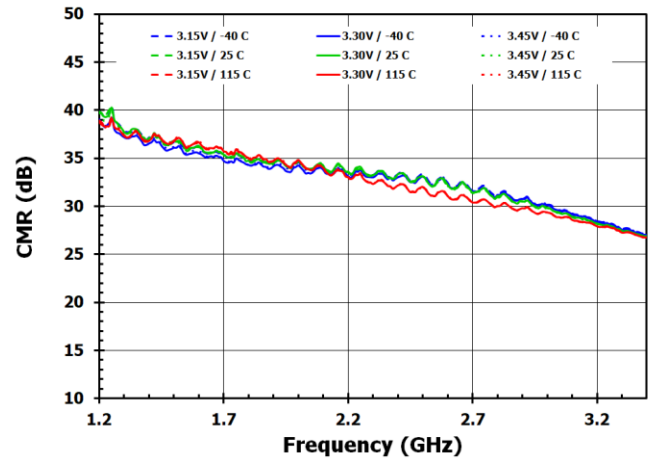
**Figure 20. Amplitude Imbalance**



**Figure 21. Phase Imbalance**



**Figure 22. CMR**



Evaluation Kit Picture

Figure 23. Evaluation Kit – Top View

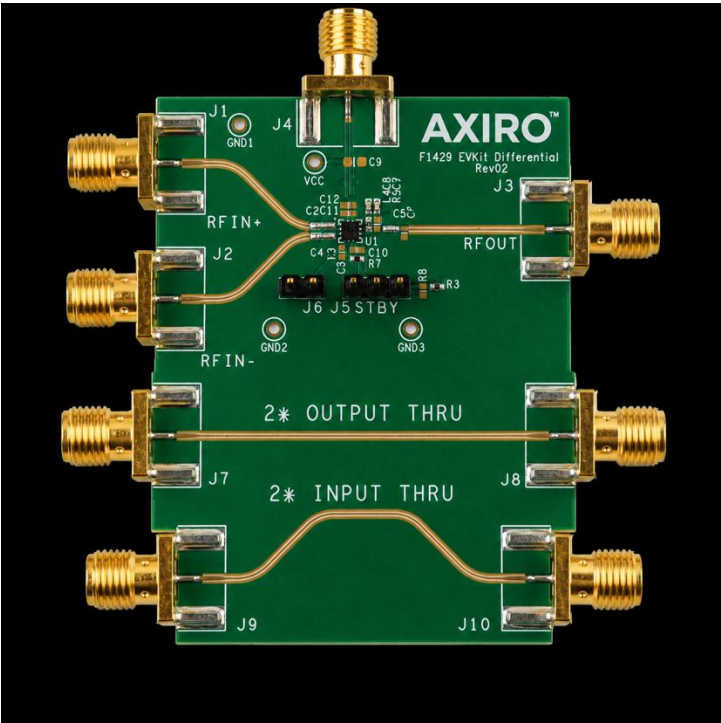
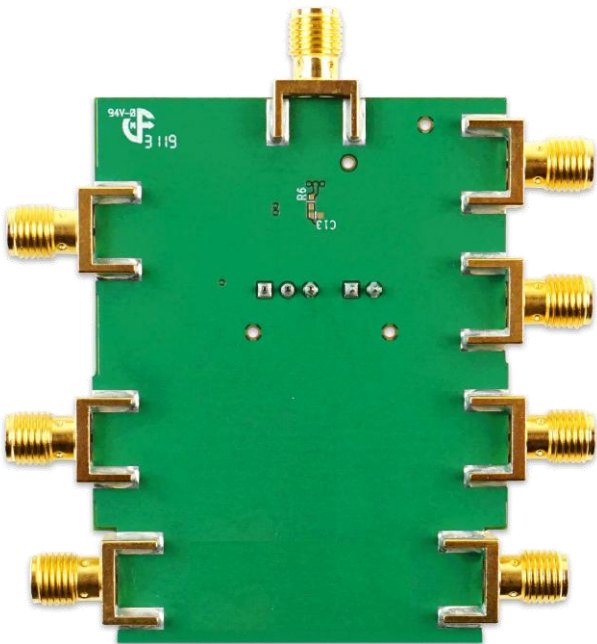
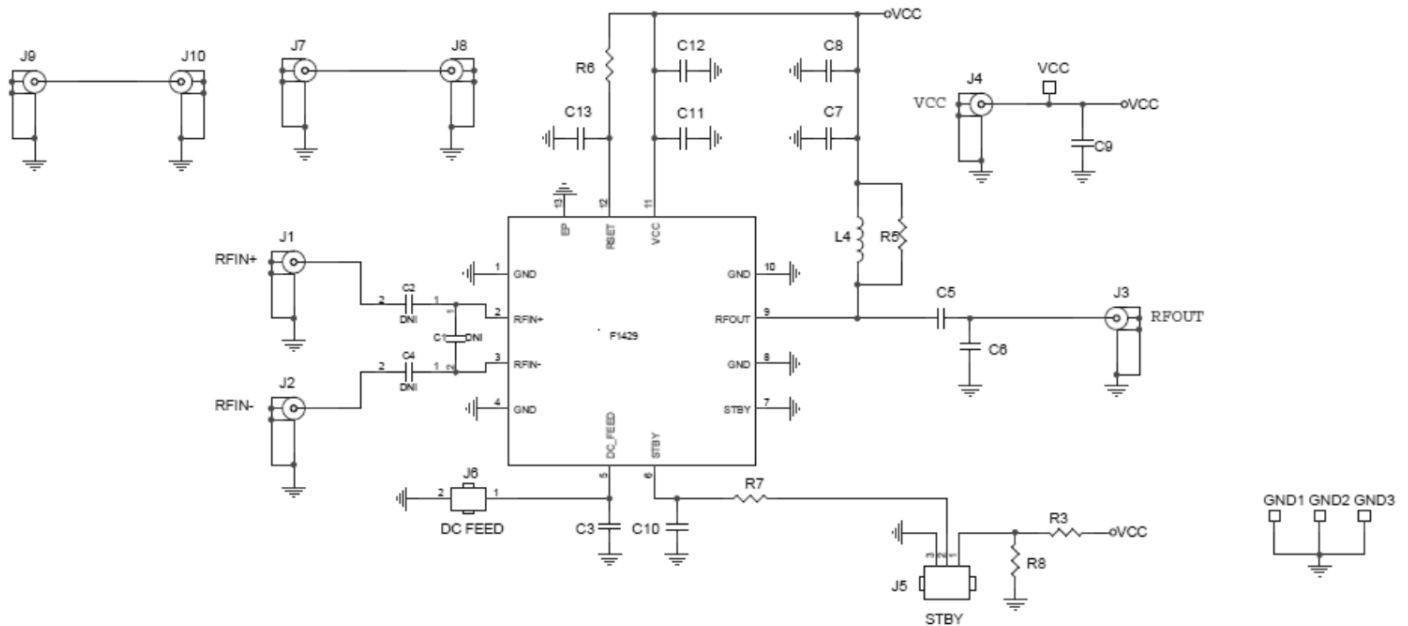


Figure 24. Evaluation Kit – Bottom View



## Evaluation Kit Circuit

**Figure 25. Evaluation Kit Electrical Schematic**



**Table 7. Evaluation Kit Bill of Material (BOM)**

| Part Reference                         | QTY | Description                                 | Manufacturer Part # | Manufacturer       |
|----------------------------------------|-----|---------------------------------------------|---------------------|--------------------|
| R7,R3                                  | 2   | 0 ohm Jumper 1/10W (0402)                   | ERJ2GE0R00X         | Panasonic          |
| C8                                     | 1   | 1000pF C0G 50v (0402)                       | GRM1555C1H102J      | Murata             |
| C11                                    | 1   | Do not Install                              |                     |                    |
| C7                                     | 1   | 100pF C0G 50v (0402)                        | GRM1555C1H101J      | Murata             |
| C2, C4                                 | 2   | 15pF C0G 50v (0402)                         | GJM1555C1H150J      | Murata             |
| C10                                    | 1   | Do not Install                              |                     |                    |
| L4                                     | 1   | 3.6nH +/-0.3 (0402)                         | LQG15HS3N6S02       | Murata             |
| C5                                     | 1   | 2.5pF COG 50V (0402)                        | GRM1555C1H2R5B      | Murata             |
| J5                                     | 1   | CONN HEADER VERT 1x3 POS 2.54MM             | 61300311121         | Würth Elektronik   |
| J1,J2, J3, J4, J7, J8, J9,J10          | 6   | SMA Edge Mount                              | 142-0701-851        | Cinch Connectivity |
| C1,C12, C3, C6, C9, R2, R5, R6, R8, J6 | 0   | Do not Install                              |                     |                    |
| U1                                     | 1   | F1429LB 100Ω DIFF-In — 50Ω SE-Out Amplifier | F1429LBNELI         | Axiro              |

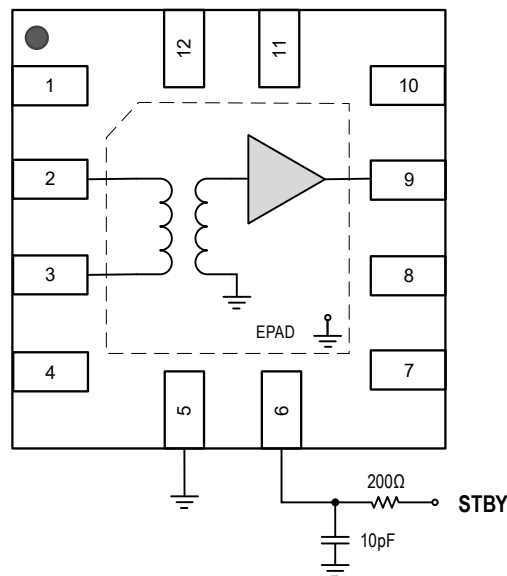
## Application Information

### Power Supplies

A common VCC power supply should be used for all pins requiring DC power. All supply pins should be bypassed with external capacitors to minimize noise and fast transients. Supply noise can degrade noise figure and fast transients can trigger ESD clamps and cause them to fail. Supply voltage change or transients should have a slew rate smaller than  $1\text{V}/20\mu\text{S}$ . In addition, all control pins should remain at  $0\text{V}$  ( $\pm 0.3\text{V}$ ) while the supply voltage ramps or while it returns to zero.

If control signal integrity is a concern and clean signals cannot be guaranteed due to overshoot, undershoot, ringing, etc., the following circuit at the input of the STBY control pin is recommended. This applies to the STBY pin as shown below. Note the recommended resistor and capacitor values do not necessarily match the EVKit BOM for the case of poor control signal integrity. For multiple devices driven by a single control line, the component values will need to be adjusted accordingly so as not to load down the control line.

**Figure 26. Control Pin Interface for Signal Integrity**



## Package Outline Drawings

The [package outline drawings](#) are accessible from the Axiro website. The package information is the most current data available and is subject to change without revision of this document.

## Marking Diagram

| Top Marking Illustration                                                          | Marking | Representation         |
|-----------------------------------------------------------------------------------|---------|------------------------|
|  | 14LB    | Part Number            |
|                                                                                   | Y       | Last Digit of the Year |
|                                                                                   | W       | Work Week              |
|                                                                                   | **      | Lot Sequential Code    |

## Ordering Information

| Orderable Part Number | Package                | MSL Rating | Carrier Type | Temperature    |
|-----------------------|------------------------|------------|--------------|----------------|
| F1429LBNELI           | 2 × 2 × 0.75 mm 12-DFN | 1          | Cut Reel     | -40° to +115°C |
| F1429LBNELI8          | 2 × 2 × 0.75 mm 12-DFN | 1          | Reel         | -40° to +115°C |
| F1429LBEVB            | Evaluation Board       |            |              |                |

## Revision History

| Revision | Date         | Description of Change                                                                                                         |
|----------|--------------|-------------------------------------------------------------------------------------------------------------------------------|
| 2.00     | Jul 31, 2026 | ▪ Updated document branding and layout to Axiro Semiconductor standard. No changes to device functionality or specifications. |